UDA.022



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

e U.S. Patent No. 7,098,729 B2

Osamu ABE

Serial No.:

10/647,468

Group Art Unit:

2816

Filing Date:

August 26, 2003

Examiner:

Englund, Terry Lee

For:

BAND GAP CIRCUIT

Honorable Commissioner of Patents Alexandria, Virginia 22313-1450 Certificate

OCT 1 8 2006

Of Correction

REQUEST FOR CERTIFICATE OF CORRECTION

Sir:

The undersigned respectfully requests a Certificate of Correction for the above-identified patent. In particular, it is requested that a typographical error in the Assignees's name be corrected to:

NEC Electronics Corporation

Since this error is believed due to the Patent and Trademark Office, no fee is submitted herewith.

Please forward a certificate of Correction to the address shown below. If there are any questions on this matter, please direct all telephone calls to the number shown below.

The Commissioner is authorized charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481

Respectfully submitted,

Sean M. McGinn

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.: 7,098,729 B2

DATED : August 29, 2006

INVENTOR(S): Osamu ABE

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

It has been requested that the assignee name be corrected to NEC Electronics Corporation.

(73) Assignee: NEC Electronics Corporation,

Kanagawa (JP)

MAILING ADDRESS OF SENDER (Please do not use customer number

PATENT NO. 7,098,729 B2

No. of additional copies

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P15/REV03



US007098729B2

(12) United States Patent Abe

(10) Patent No.:

US 7,098,729 B2

(45) Date of Patent:

Aug. 29, 2006

(54)	BAND GAP CIRCUIT				
(75)	Inventor:	Osamu Abe. Kanagawa (JP)			
(73)	Assignee:	NEC <u>Electronics</u> Corporation. Kanagawa (JP) Electronics			
(*)	Notice:	Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.			
(21)	Appl. No.: 10/647,468				
(22)	Filed:	Aug. 26, 2003			
(65)	Prior Publication Data				
	US 2004/0051581 A1 Mar. 18, 2004				
(30)	Foreign Application Priority Data				
Aug. 28, 2002 (JP) 2002-249352					
(51)	G05F 1/57	(2006.01) (2006.01)			
	U.S. Cl				
(58)	Field of Classification Search 327/539				
	323/313 See application file for complete search history.				
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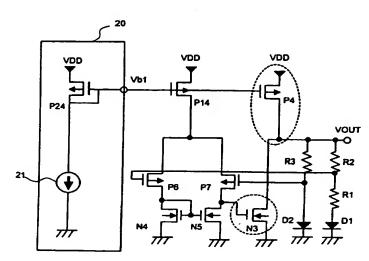
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(57) ABSTRACT

A band gap circuit, including a differential amplifier. In response to fluctuation of the voltage on the band gap circuit output terminal VOUT, a potential difference occurs at an inverting input terminal and a noninverting input terminal of the differential amplifier. A transistor, connected to the output terminal VOUT, ground, an output terminal of the differential amplifier, causes excess current from the output terminal VOUT to flow to ground in response to fluctuation of the potential at the output terminal of the differential amplifier. A transistor that has a resistive component and a resistor that has a capacitive component are connected between a power supply voltage VDD and the output terminal VOUT.

16 Claims, 8 Drawing Sheets



OCT 18 2006